APPLICATION NO. CONT/PRIOR: CLASS SUBCLASS ART UNIT 243 410 306 2181  Richard Zatorski	EXAMINER Lefterent Z
09/912745 713710 306 2181	EXAMINER Lefterent Z
Richard Zatorski	
Richard Zatorski	<u>u</u>
Bus bridge circuit including audio logic and register for storing an address bit used wher accesses digital data, and method for initial including the bus bridge circuit	an addressable o the audio logic lizing a chip set
including the bus bridge circuit	PTO-2040 12/99
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